

8/23/9
4-17-02

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application Of

Ken Takeuchi et al.

Divisional of Serial No.: 09/667,610

Filed: Herewith

10/073,999

For: NONVOLATILE SEMICONDUCTOR
MEMORY HAVING PLURAL DATA
STORAGE PORTIONS FOR A BIT LINE
CONNECTED TO MEMORY CELLS

Group Art Unit: 2818

Examiner: H. Ho

Atty. Dkt. No. 001701.00140

The Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

PRELIMINARY AMENDMENT

The Applicants request that the following amendments be entered prior to an action on the merits.

IN THE TITLE:

Please delete the existing title in its entirety and replace it with the following Title:

--NONVOLATILE SEMICONDUCTOR MEMORY HAVING PLURAL DATA STORAGE
PORTIONS FOR A BIT LINE CONNECTED TO MEMORY CELLS--.

IN THE SPECIFICATION:

Please insert on line 1, --This application is a Divisional of U.S. Application Serial No.

Now U.S. PAT. NO. 6,373,746 B1

09/667,610 filed on September 22, 2000, hereby incorporated by reference as to its entirety.--.

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